

**SAURASHTRA UNIVERSITY, RAJKOT**  
**B.E. 1<sup>ST</sup> YEAR ( EC/CE/IT/BM/EEE)**

Code	Subject	Teaching Scheme		Examination Scheme			
		Lect.	Pract.	Theory	Paper Hrs.	Term work/oral	Total
101	Communication Skills	2	-	50	2	25	75
102	Mathematics	3	-	100	3	-	100
103	Engineering Graphics	2	2	100	3	50	150
104	Material Science	2	-	100	3	-	100
105	Principles of Electrical Engineering	2	2	100	3	25	125
106	Fundamentals of Electronics	2	2	100	3	25	125
107	Computer Programming-I	2	2	100	3	25	125
108	Principles of Structural and Mech. Engg.	2	2	100	3	50	150
109	Electronics Practice	1	2	-	-	50	50
	Total	18	12	750	-	250	1000

**SAURASHTRA UNIVERSITY, RAJKOT**

**SEMESTER III B.E.**

**[ ELECTRICAL & ELECTRONICS ENGINEERING ]**

Code	Subject	Teaching Scheme		Examination Scheme				
		Lect .	Pract.	Theory	Pape r Hrs.	Pract/oral	Term work	Total
301	Engineering Mathematics	4	-	100	3	-	-	100
302	Programming Techniques	4	2	100	3	25	25	150
303	Electrical Machine-I	4	2	100	3	25	25	150
304	Electrical Measurement	4	2	100	3	25	25	150
305	Principles of Electronics Circuits	4	2	100	3	25	25	150
306	Electrical Workshop	-	2	-		25	25	050
	Total	20	10	500	-	125	125	750

**SAURASHTRA UNIVERSITY**

**SYLLABUS  
FOR  
ELECTRICAL & ELECTRONICS ENGINEERING**

**B.E. SEMESTER IV  
TEACHING AND EXAMINATION SCHEME  
PROPOSED  
YEAR OF IMPLEMENTATION: 2004-2005**

Code	Subject	Teaching Scheme		Examination Scheme				
		Lect .	Pract.	Theory	Pape r Hrs.	Pract/oral	Term work	Total
401	Electrical Power System	4	2	100	3	25	25	150
402	Analog Integrated Circuits	4	2	100	3	25	25	150
403	Network Analysis	4	2	100	3	25	25	150
404	Principles of Control System	4	2	100	3	25	25	150
405	Digital Circuits & Design	4	2	100	3	25	25	150
	Total	20	10	500	-	125	125	750

**SAURASHTRA UNIVERSITY**

**B.E SEMESTER V**

**TEACHING AND EXAMINATION SCHEM**

**YEAR OF IMPLEMENTATION :2005-2006**

<b>Code</b>	<b>subject</b>	<b>Teaching scheme</b>		<b>Examination Scheme</b>				
		<b>Lect. Hrs</b>	<b>Pract. Hrs</b>	<b>Theory marks</b>	<b>Paper Hrs</b>	<b>Oral\ Pract.</b>	<b>T.W</b>	<b>Total marks</b>
501	Electronics Measurement	4	2	100	3	50	25	175
502	Digital Control System	4	2	100	3	50	25	175
503	Electrical Machine-II	4	2	100	3	50	25	175
504	Electronics Communication	4	2	100	3	50	25	175
505	Engineering Electromagnetics	4	-	100	3	-	-	100
506	Electronics Workshop	-	2	-	-	50	25	75
	<b>Total</b>	<b>20</b>	<b>10</b>	<b>500</b>		<b>250</b>	<b>125</b>	<b>875</b>

**# One Industrial Visit Be held during the semester**

**SAURASHTRA UNIVERSITY**

**PROPOSED**

**SYLLABUS**

**FOR**

**ELECTRICAL & ELECTRONICS ENGINEERING**

**B.E. SEMESTER VI**

**TEACHING AND EXAMINATION SCHEME**

**YEAR OF IMPLEMENTATION: 2005-06**

<b>Code</b>	<b>Subject</b>	<b>Teaching Scheme</b>		<b>Examination scheme</b>				
		<b>Lect. Hrs.</b>	<b>Prac. Hrs.</b>	<b>Theory Marks</b>	<b>Paper hrs.</b>	<b>Oral/ prac.</b>	<b>T.W.</b>	<b>Total Marks</b>
601	Industrial management & operation research	4	-	100	3	-	-	100
602	Power plant control & instrumentation	4	2	100	3	50	25	175
603	Microprocessors & micro-controller	4	2	100	3	50	25	175
604	Utilization of electrical energy	4	2	100	3	50	25	175
605	Power transmission & distribution	4	2	100	3	50	25	175
606	Seminar	-	2	-	-	50	25	75
	<b>Total</b>	20	10	500	-	250	125	875

**One industrial visit be held during the semester college level examination**

**SAURASHTRA UNIVERSITY**

**B.E SEMESTER VII**

**TEACHING AND EXAMINATION SCHEM**

**YEAR OF IMPLEMENTATION :2005-2006**

<b>Code</b>	<b>subject</b>	<b>Teaching scheme</b>		<b>Examination Scheme</b>				
		Lect. Hrs	Pract. Hrs	Theory marks	Paper Hrs	Oral\ Pract.	T.W	Total marks
701	VLSI Technologies	4	2	100	3	50	25	175
702	Power System Analysis	4	2	100	3	50	25	175
703	Power Electronics	4	2	100	3	50	25	175
704	Electronics System Design	4	2	100	3	50	25	175
705	Switchgear & Protection	4	2	100	3	50	25	175
	Total	20	10	500	15	250	125	875

**SAURASHTRA UNIVERSITY**

**B.E SEMESTER VIII**

**TEACHING AND EXAMINATION SCHEM**

**YEAR OF IMPLEMENTATION :2005-2006**

<b>Code</b>	<b>subject</b>	<b>Teaching scheme</b>		<b>Examination Scheme</b>				
		Lect. Hrs	Pract. Hrs	Theory marks	Paper Hrs	Oral\ Pract.	T.W	Total marks
801	Electrical Machine Design	4	2	100	3	50	25	175
802	Digital Signal Processing	4	2	100	3	50	25	175
803	Industrial Electronics	4	2	100	3	50	25	175
804	Comm. Of Elect. & Electronics Equipments	4	2	100	3	50	25	175
805	Project	-	6	-	-	125	50	175
	Total	16	14	400	12	325	150	875

